1. CN14 = RD5

TRISDbits.TRISD5 = 1

CNPUEbits.CNPUE14 = 1

* 1. Core timer is 32 bit and timer 2 is 16 by default
  2. The core timer is from the cpu clock and timer 2 is a peripheral

1. Max number of pulses = 2^16 \* 256 = 16777216

N = 256

PR1 = Max\_pulses / N – 1 = 65535

* 1. PR2 = 19999
  2. 4000

1. The two steps are sampling and conversion
   1. Sampling uses the ADC to charge the holding capacitor to V\_inh – V\_inl.
   2. Conversion uses the SAR uses 10\_bit search to approximate V\_sha and writes that to the buffers

A piece of paper with writing on it

Description automatically generated